

Figure 1

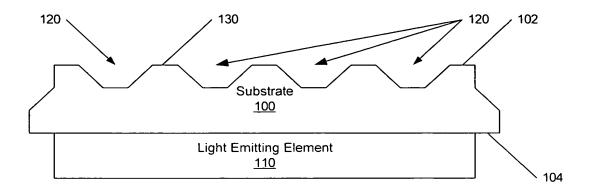
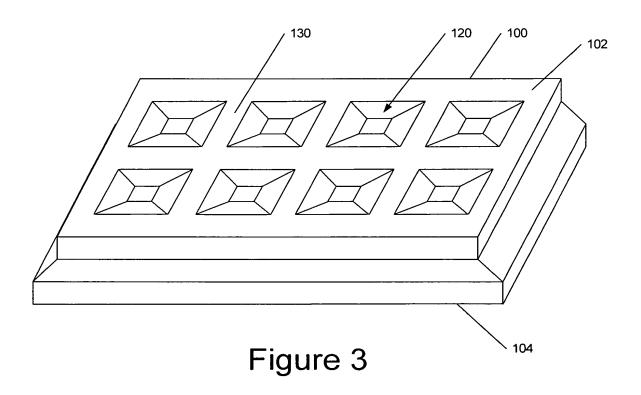


Figure 2



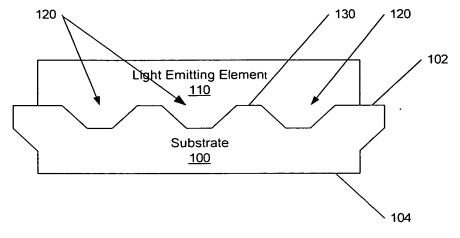


Figure 4

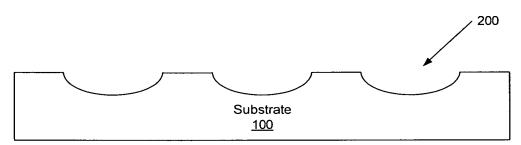


Figure 5

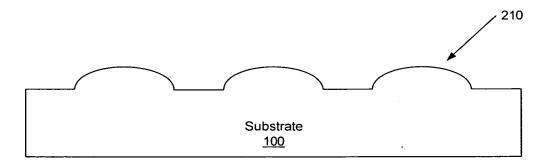


Figure 6

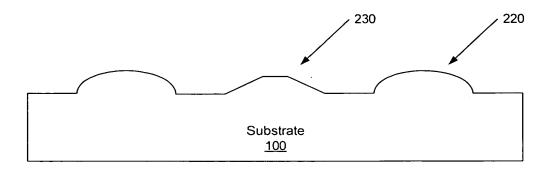


Figure 7

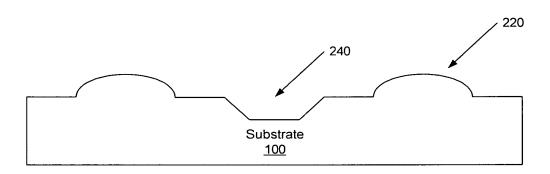


Figure 8

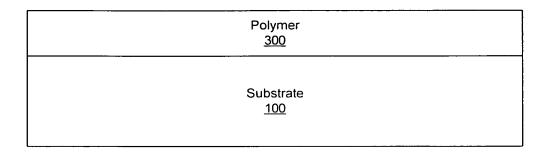


Figure 9

Substrate
100

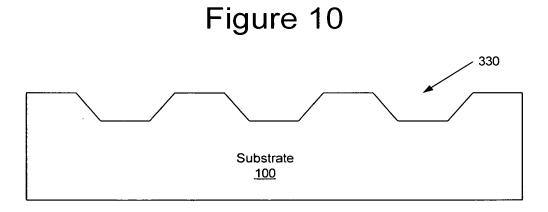


Figure 11

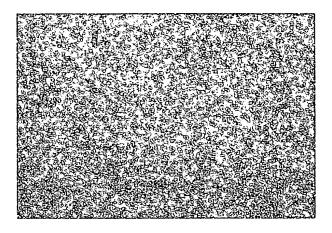


Figure 12A

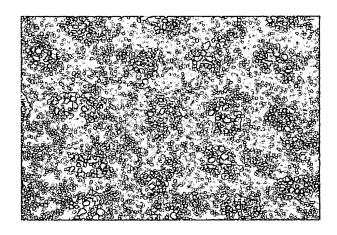


Figure 12B

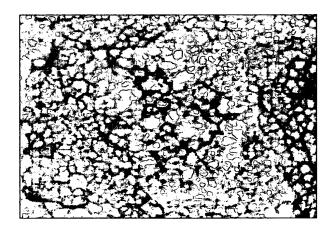


Figure 12C

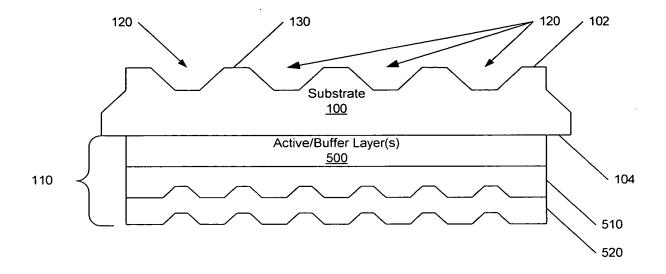


Figure 13

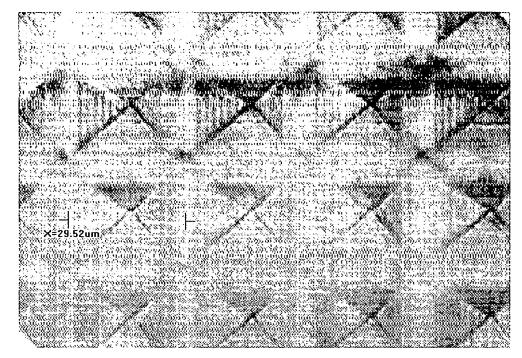


Figure 14A

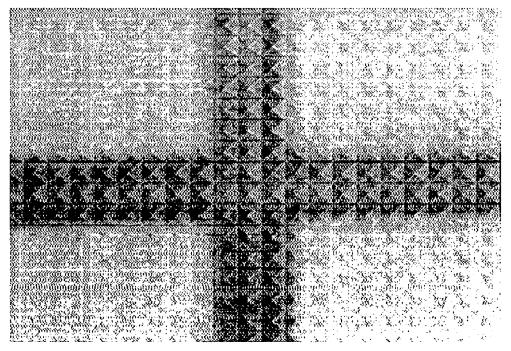


Figure 14B

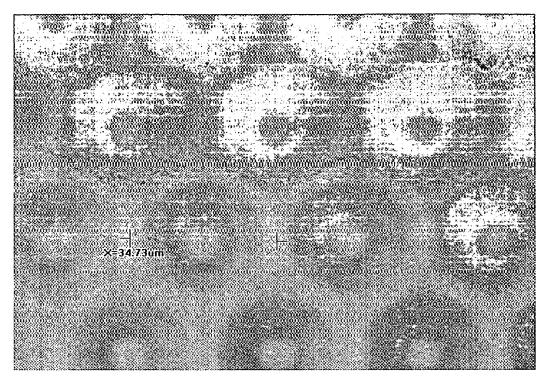


Figure 15A

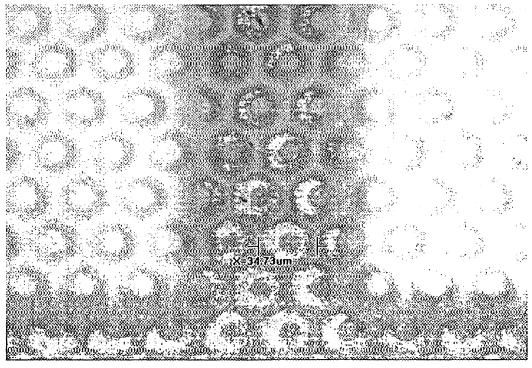


Figure 15B